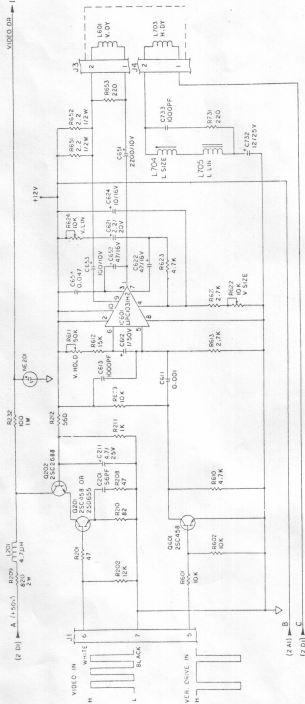




MC

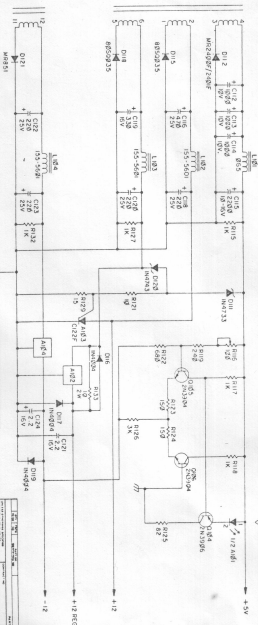
1002-1011



CRMD	T2	
L4	S62	
UL	DSI	
Q9	F1	
C3I	J4	
RSI	P2	R2
LAST USED		NOT USED
REFERENCE DESIGNATIONS		

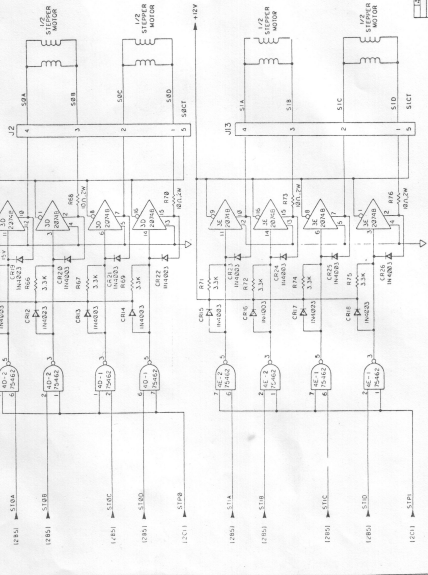
[illegible]

2 ALL RESISTORS ARE $\pm 5\%$ 1/4 W.
1 ELECTRICAL VALUES ARE IN OHMS, MICROFARADS
AND MICROHENRIES.
NOTES: UNLESS OTHERWISE SPECIFIED:



- NOTES: UNLESS OTHERWISE SPECIFIED,

1987-1988 1989-1990 1991-1992 1993-1994 1995-1996 1997-1998 1999-2000 2001-2002 2003-2004 2005-2006 2007-2008 2009-2010 2011-2012 2013-2014 2015-2016 2017-2018 2019-2020 2021-2022 2023-2024 2025-2026 2027-2028 2029-2030 2031-2032 2033-2034 2035-2036 2037-2038 2039-2040 2041-2042 2043-2044 2045-2046 2047-2048 2049-2050 2051-2052 2053-2054 2055-2056 2057-2058 2059-2060 2061-2062 2063-2064 2065-2066 2067-2068 2069-2070 2071-2072 2073-2074 2075-2076 2077-2078 2079-2080 2081-2082 2083-2084 2085-2086 2087-2088 2089-2090 2091-2092 2093-2094 2095-2096 2097-2098 2099-2100 2101-2102 2103-2104 2105-2106 2107-2108 2109-2110 2111-2112 2113-2114 2115-2116 2117-2118 2119-2120 2121-2122 2123-2124 2125-2126 2127-2128 2129-2130 2131-2132 2133-2134 2135-2136 2137-2138 2139-2140 2141-2142 2143-2144 2145-2146 2147-2148 2149-2150 2151-2152 2153-2154 2155-2156 2157-2158 2159-2160 2161-2162 2163-2164 2165-2166 2167-2168 2169-2170 2171-2172 2173-2174 2175-2176 2177-2178 2179-2180 2181-2182 2183-2184 2185-2186 2187-2188 2189-2190 2191-2192 2193-2194 2195-2196 2197-2198 2199-2200 2201-2202 2203-2204 2205-2206 2207-2208 2209-2210 2211-2212 2213-2214 2215-2216 2217-2218 2219-2220 2221-2222 2223-2224 2225-2226 2227-2228 2229-2230 2231-2232 2233-2234 2235-2236 2237-2238 2239-2240 2241-2242 2243-2244 2245-2246 2247-2248 2249-2250 2251-2252 2253-2254 2255-2256 2257-2258 2259-2260 2261-2262 2263-2264 2265-2266 2267-2268 2269-2270 2271-2272 2273-2274 2275-2276 2277-2278 2279-2280 2281-2282 2283-2284 2285-2286 2287-2288 2289-2290 2291-2292 2293-2294 2295-2296 2297-2298 2299-2300 2301-2302 2303-2304 2305-2306 2307-2308 2309-2310 2311-2312 2313-2314 2315-2316 2317-2318 2319-2320 2321-2322 2323-2324 2325-2326 2327-2328 2329-2330 2331-2332 2333-2334 2335-2336 2337-2338 2339-2340 2341-2342 2343-2344 2345-2346 2347-2348 2349-2350 2351-2352 2353-2354 2355-2356 2357-2358 2359-2360 2361-2362 2363-2364 2365-2366 2367-2368 2369-2370 2371-2372 2373-2374 2375-2376 2377-2378 2379-2380 2381-2382 2383-2384 2385-2386 2387-2388 2389-2390 2391-2392 2393-2394 2395-2396 2397-2398 2399-2400 2401-2402 2403-2404 2405-2406 2407-2408 2409-2410 2411-2412 2413-2414 2415-2416 2417-2418 2419-2420 2421-2422 2423-2424 2425-2426 2427-2428 2429-2430 2431-2432 2433-2434 2435-2436 2437-2438 2439-2440 2441-2442 2443-2444 2445-2446 2447-2448 2449-2450 2451-2452 2453-2454 2455-2456 2457-2458 2459-2460 2461-2462 2463-2464 2465-2466 2467-2468 2469-2470 2471-2472 2473-2474 2475-2476 2477-2478 2479-2480 2481-2482 2483-2484 2485-2486 2487-2488 2489-2490 2491-2492 2493-2494 2495-2496 2497-2498 2499-2500 2501-2502 2503-2504 2505-2506 2507-2508 2509-2510 2511-2512 2513-2514 2515-2516 2517-2518 2519-2520 2521-2522 2523-2524 2525-2526 2527-2528 2529-2530 2531-2532 2533-2534 2535-2536 2537-2538 2539-2540 2541-2542 2543-2544 2545-2546 2547-2548 2549-2550 2551-2552 2553-2554 2555-2556 2557-2558 2559-2560 2561-2562 2563-2564 2565-2566 2567-2568 2569-	
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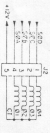


STEPPER CONTROL

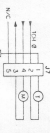
DATE	10/10/71	BY	W. J. L. P.
DESIGNED BY	W. J. L. P.	CHECKED BY	W. J. L. P.
APPROVED BY	W. J. L. P.	DATE	10/10/71
PROJECT	STEPPER CONTROL	REV	1
DESCRIPTION	STEPPER CONTROL	REV	1
DATE	10/10/71	BY	W. J. L. P.
DESIGNED BY	W. J. L. P.	CHECKED BY	W. J. L. P.
APPROVED BY	W. J. L. P.	DATE	10/10/71
PROJECT	STEPPER CONTROL	REV	1
DESCRIPTION	STEPPER CONTROL	REV	1

DATE	10/10/71	BY	W. J. L. P.
DESIGNED BY	W. J. L. P.	CHECKED BY	W. J. L. P.
APPROVED BY	W. J. L. P.	DATE	10/10/71
PROJECT	STEPPER CONTROL	REV	1
DESCRIPTION	STEPPER CONTROL	REV	1

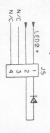
DATE	10/10/71	BY	W. J. L. P.
DESIGNED BY	W. J. L. P.	CHECKED BY	W. J. L. P.
APPROVED BY	W. J. L. P.	DATE	10/10/71
PROJECT	STEPPER CONTROL	REV	1
DESCRIPTION	STEPPER CONTROL	REV	1



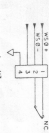
STEPPER A
(SHIFT 9)



MOTOR A (18V, DC, DISCRETE)
(SHIFT 3)



ACTIVITY LED A
(SHIFT 6)



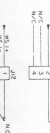
WRITE PROTECT A
(SHIFT 6)



STEPPER B
(SHIFT 9)



MOTOR B
(SHIFT 3)

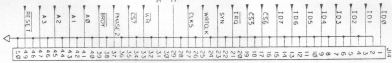


ACTIVITY LED B
(SHIFT 6)



WRITE PROTECT B
(SHIFT 6)

CPU INTERFACE (SLS2)



POWER
CONNECTOR
(SHIFT 1)



DRIVE A
HEAD 1
(SHIFT 3)



DRIVE A
HEAD 1
(SHIFT 3)



DRIVE B
HEAD 1
(SHIFT 3)



DRIVE B
HEAD 1
(SHIFT 3)



DRIVE A
HEAD 1
(SHIFT 3)



DRIVE A
HEAD 1
(SHIFT 3)



DRIVE B
HEAD 1
(SHIFT 3)



DRIVE B
HEAD 1
(SHIFT 3)



MOTOR
SWITCH A
(SHIFT 6)



DISK A
(SHIFT 2)



DISK B
(SHIFT 2)



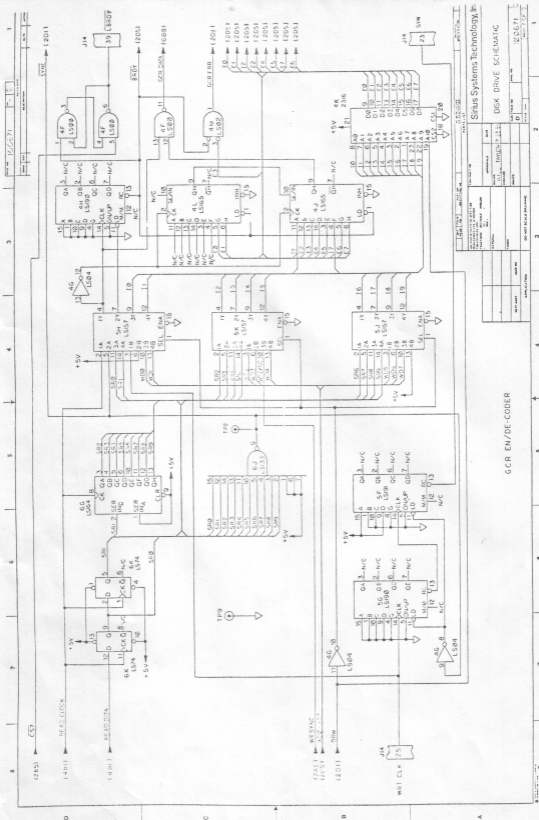
DISK B
(SHIFT 2)

DISK CONNECTORS (REF)

Stratus Systems Technology, Inc.

DISK DRIVE SCHEMATIC

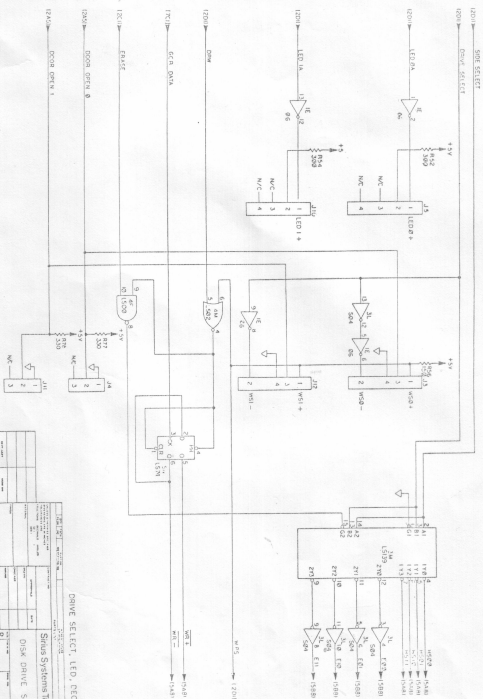
Connector	Pin	Signal	Notes
J1	1	+5V	
J1	2	-5V	
J1	3	NC	
J1	4	NC	
J1	5	NC	
J1	6	NC	
J1	7	NC	
J1	8	NC	
J1	9	NC	
J1	10	NC	
J1	11	NC	
J1	12	NC	
J1	13	NC	
J1	14	NC	
J1	15	NC	
J1	16	NC	
J1	17	NC	
J1	18	NC	
J1	19	NC	
J1	20	NC	
J1	21	NC	
J1	22	NC	
J1	23	NC	
J1	24	NC	
J1	25	NC	



GCR EN/DE-CODER

DGK DRIVE SCHEMATIC

Sinus Systems Technology

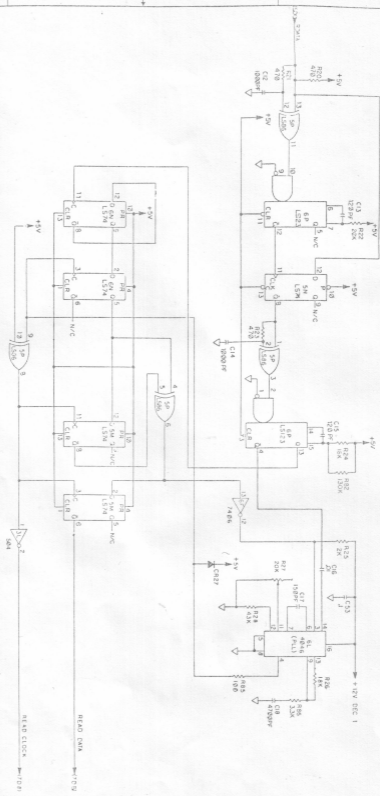


DRIVE SELECT, LED, DECODE

DISK DRIVE SCHEMATIC

ITEM NO.	DESCRIPTION	QTY	UNIT PRICE	TOTAL PRICE
1	74139	1	1.00	1.00
2	7410	1	1.00	1.00
3	7414	1	1.00	1.00
4	LED 1A	1	1.00	1.00
5	LED 1B	1	1.00	1.00
6	LED 1C	1	1.00	1.00
7	LED 1D	1	1.00	1.00
8	LED 1E	1	1.00	1.00
9	LED 1F	1	1.00	1.00
10	LED 1G	1	1.00	1.00
11	LED 1H	1	1.00	1.00
12	LED 1I	1	1.00	1.00
13	LED 1J	1	1.00	1.00
14	LED 1K	1	1.00	1.00
15	LED 1L	1	1.00	1.00
16	LED 1M	1	1.00	1.00
17	LED 1N	1	1.00	1.00
18	LED 1O	1	1.00	1.00
19	LED 1P	1	1.00	1.00
20	LED 1Q	1	1.00	1.00
21	LED 1R	1	1.00	1.00
22	LED 1S	1	1.00	1.00
23	LED 1T	1	1.00	1.00
24	LED 1U	1	1.00	1.00
25	LED 1V	1	1.00	1.00
26	LED 1W	1	1.00	1.00
27	LED 1X	1	1.00	1.00
28	LED 1Y	1	1.00	1.00
29	LED 1Z	1	1.00	1.00

REV	DATE	BY	CHKD	APP'D
1				

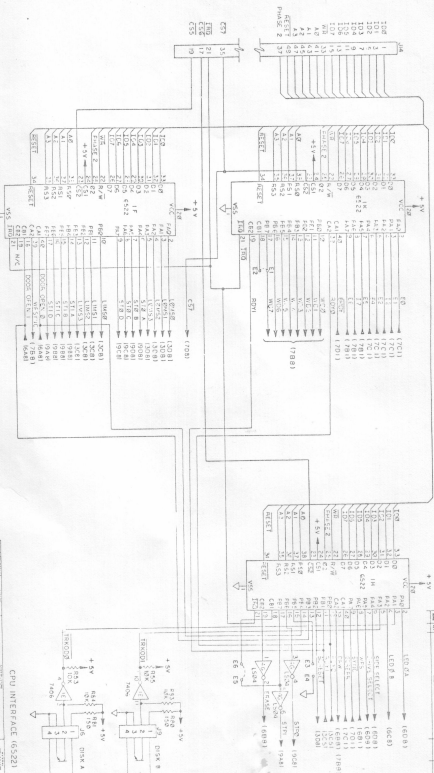


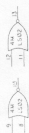
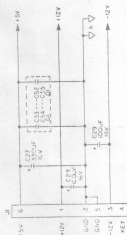
PLL

REV	DATE	BY	CHKD	APP'D
1				

DATE	10/05/71
BY	10/05/71
CHKD	10/05/71
APP'D	10/05/71

DATE	10/05/71
BY	10/05/71
CHKD	10/05/71
APP'D	10/05/71





SPARE GATES

NOTES UNLESS OTHERWISE SPECIFIED.
1 ELECTRICAL VALUES ARE IN OHMS, MICROGRAMS AND MICROHENRIES.
2 ALL RESISTORS ARE 25%, 1/4W.
3 ALL RESISTORS ARE 25%, 1/4W.
4 Q1-Q2 ARE MOUNTED ON HEATSINK.
5 ALL INTEGRATED CIRCUITS ARE SN44 SERIES.
6 I.C. PINS ARE GND 1, 2, 4, 14 EXCEPT FOR:

TYPE	GRID	45V
1A, 1B, 1C	8	16
2A, 2B, 2C	8	16
3A, 3B, 3C	8	16
4A, 4B, 4C	8	16
5A, 5B, 5C	8	16
6A, 6B, 6C	8	16
7A, 7B, 7C	8	16
8A, 8B, 8C	8	16
9A, 9B, 9C	8	16
10A, 10B, 10C	8	16
11A, 11B, 11C	8	16
12A, 12B, 12C	8	16
13A, 13B, 13C	8	16
14A, 14B, 14C	8	16
15A, 15B, 15C	8	16
16A, 16B, 16C	8	16
17A, 17B, 17C	8	16
18A, 18B, 18C	8	16
19A, 19B, 19C	8	16
20A, 20B, 20C	8	16
21A, 21B, 21C	8	16
22A, 22B, 22C	8	16
23A, 23B, 23C	8	16
24A, 24B, 24C	8	16
25A, 25B, 25C	8	16

2. ALL DIODE ARE 1N4148.

SHEET	DESCRIPTION
1	POWER & SPARES
2	POWER INTERFACE (B522)
3	POWER INTERFACE (B522)
4	POWER INTERFACE (B522)
5	POWER INTERFACE (B522)
6	POWER INTERFACE (B522)
7	POWER INTERFACE (B522)
8	POWER INTERFACE (B522)
9	POWER INTERFACE (B522)

POWER & SPARES



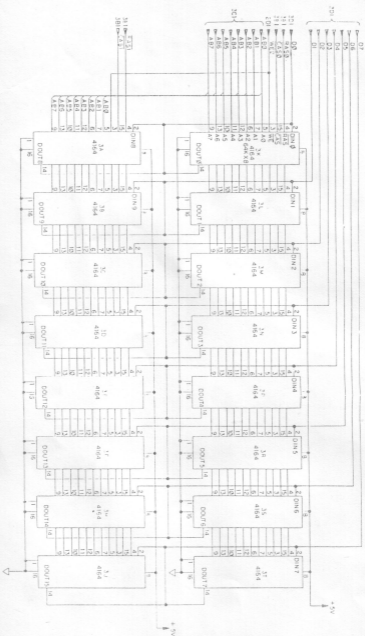
POWER & SPARES

POWER & SPARES

POWER & SPARES

POWER & SPARES

C-14



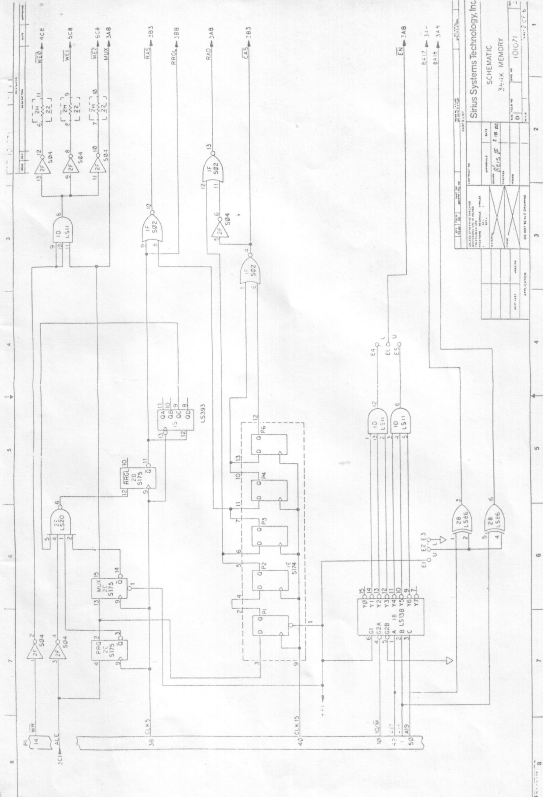
REV	DATE	BY	CHKD	APP'D
1	10/10/74			

Silicon Systems Technology Inc

SCHEMATIC

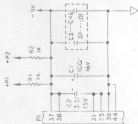
254K MEMORY

10/10/74



Sirius Systems Technology, Inc.
SCHEMATIC
3-BIT MOD/RR
10/10/71





- NOTES: UNLESS OTHERWISE SPECIFIED
1. ELECTRICAL VALUES ARE IN OHMS, MICROFARADS, AND MICROSECONDS.
 2. ALL RESISTORS ARE 5% TOL, 1/4W.
 3. ALL INTEGRATED CIRCUITS ARE SN74 SERIES.
 4. IC PINS ARE .GND 7, 5, 14

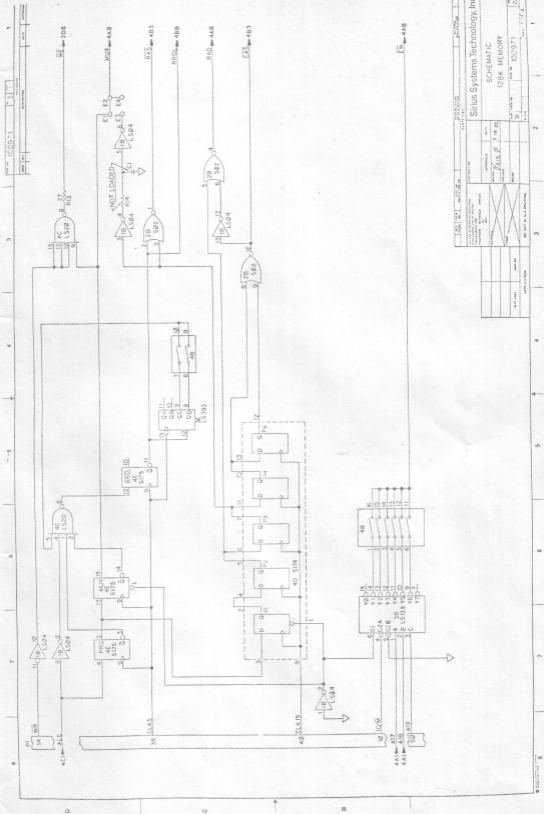
IC TYPE	GND	+5V
74LS138	8	16
74LS153	8	16
74LS157	8	16
74LS174	8	16
74LS175	8	16
74LS245	10	20
74LS373	10	20
4164	16	8

(REF)

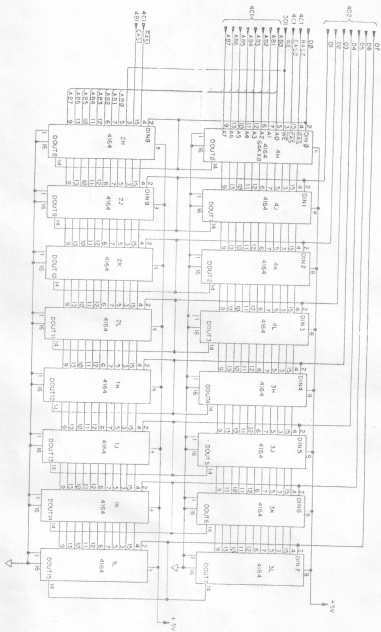


SIRIUS SYSTEMS TECHNOLOGY, INC.
 SCHEMATIC
 3-K MEMORY
 10-1 71

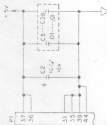
LAST USED NOT USED
 REFERENCE DESIGNATION



SILUS SYSTEMS TECHNOLOGY, INC.	
SCHEMATIC	
128K MEMORY	
DATE: 10-1-71	DESIGN: 100971
BY: [Signature]	CHECKED: [Signature]
APPROVED: [Signature]	DATE: 10-1-71



DATE: 11/1/71		DRAWN BY: J. L. HARRIS	
CHECKED BY: J. L. HARRIS		APPROVED BY: J. L. HARRIS	
TITLE: 16-BIT PARALLEL ADDER PROJECT: 16-BIT PARALLEL ADDER SHEET: 1 OF 1			
DESIGNED BY: J. L. HARRIS DRAWN BY: J. L. HARRIS CHECKED BY: J. L. HARRIS APPROVED BY: J. L. HARRIS			
PARTS LIST: 4001 16-BIT PARALLEL ADDER 4002 16-BIT PARALLEL ADDER			
COMMENTS: 16-BIT PARALLEL ADDER			
REVISIONS: 1. 11/1/71 J. L. HARRIS			
SERIALIZED BY: J. L. HARRIS INDEXED BY: J. L. HARRIS FILED BY: J. L. HARRIS			
SERIALIZED: 11/1/71 INDEXED: 11/1/71 FILED: 11/1/71			



NOTES: UNLESS OTHERWISE SPECIFIED

1. ELECTRICAL VALUES ARE IN CHMS. MICROFARADS. AND MICRO-SECS.
2. ALL RESISTORS ARE $\pm 5\%$, 1/4W
3. ALL INTEGRATED CIRCUITS ARE SN74 SERIES.
4. IC PINS ARE: GND=7, +5V=14

IC Type	GND	+5V
74LS138	8	16
74LS139	8	16
74LS153	8	16
74LS157	8	16
74LS174	8	16
74LS175	8	16
74LS245	10	20
74LS373	10	20
4164	16	8





